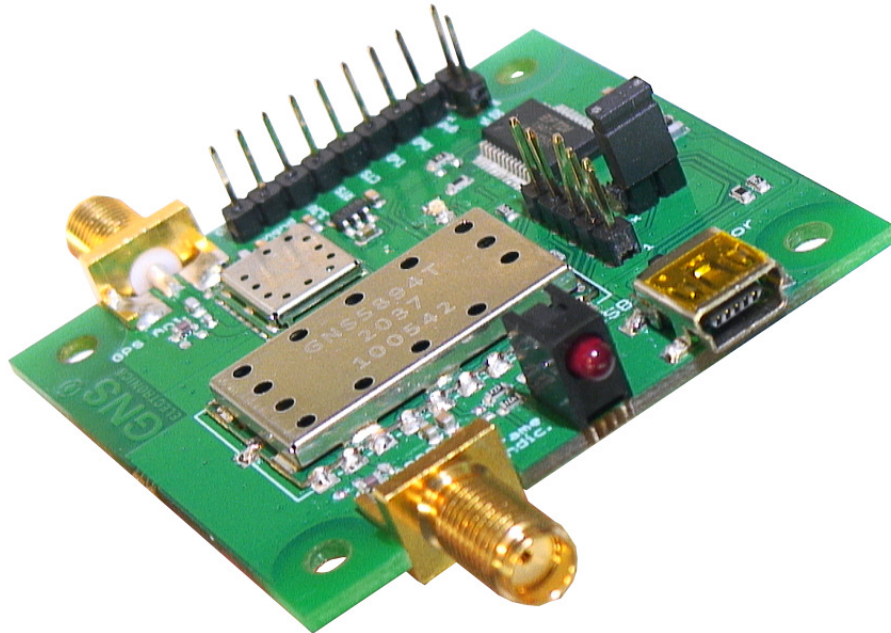




The GNS 5894TAC StarterKit has been designed to support developers during design-in of the GNS 5894TAC stand alone module solution.

Introduction

This Quick Guide is a short summary of the most important features of the GNS5894TAC StarterKit. The complete User Guide is currently not yet available and will be completed shortly.

This manual shows how to connect the GNS5894TAC StarterKit to a PC in order to verify GNS 5894TAC **Mode-S-A-C** all-in-one module performance and test supported features.

For technical specification, please refer to the **GNS5894T_ADSBModul_datasheet**.

The GNS5894TAC is a software extension of the GNS5894T, which is additionally able to receive Mode A/C data. The hardware of the GNS5894T**AC** is unchanged. For this reason the GNS5894T_ADSBModul_datasheet can be used as long as no description of the Mode A/C extension is available.

Connected to a PC, all Mode-S & Mode-A/C Data is routed to the miniUSB port.

GNS5894TAC StarterKit Features

- Power supply via Mini-USB socket
- Access to all signals of the GNS5894TAC via pin header contacts.
- PC data connection via the integrated USB to UART converter or direct access to the serial interface (3.3V) of the GNS5894TAC for connection to other data processing devices.
- 5V DC input via mini USB
- 2 SMA antenna connectors for 1090MHz and GNSS RF In
- GNSS active antenna, DC supply (3.3V)
- ADS-B "frame indicator"

Introduction	2
1 Description of the GNS 5894TAC StarterKit Hardware	4
2 GNS5894TAC StarterKit Board Layout	5
3 GNS5894TAC StarterKit Board Block Diagram	6
4 GNS5894TAC StarterKit Hardware	7
4.1 Assembly Drawing Top Side	7
4.2 5894T StarterKit Schematic.....	8
5 ORDERING INFORMATION	9
6 RELATED DOCUMENTS.....	9
7 DOCUMENT REVISION HISTORY.....	9

1 Description of the GNS 5894TAC StarterKit Hardware

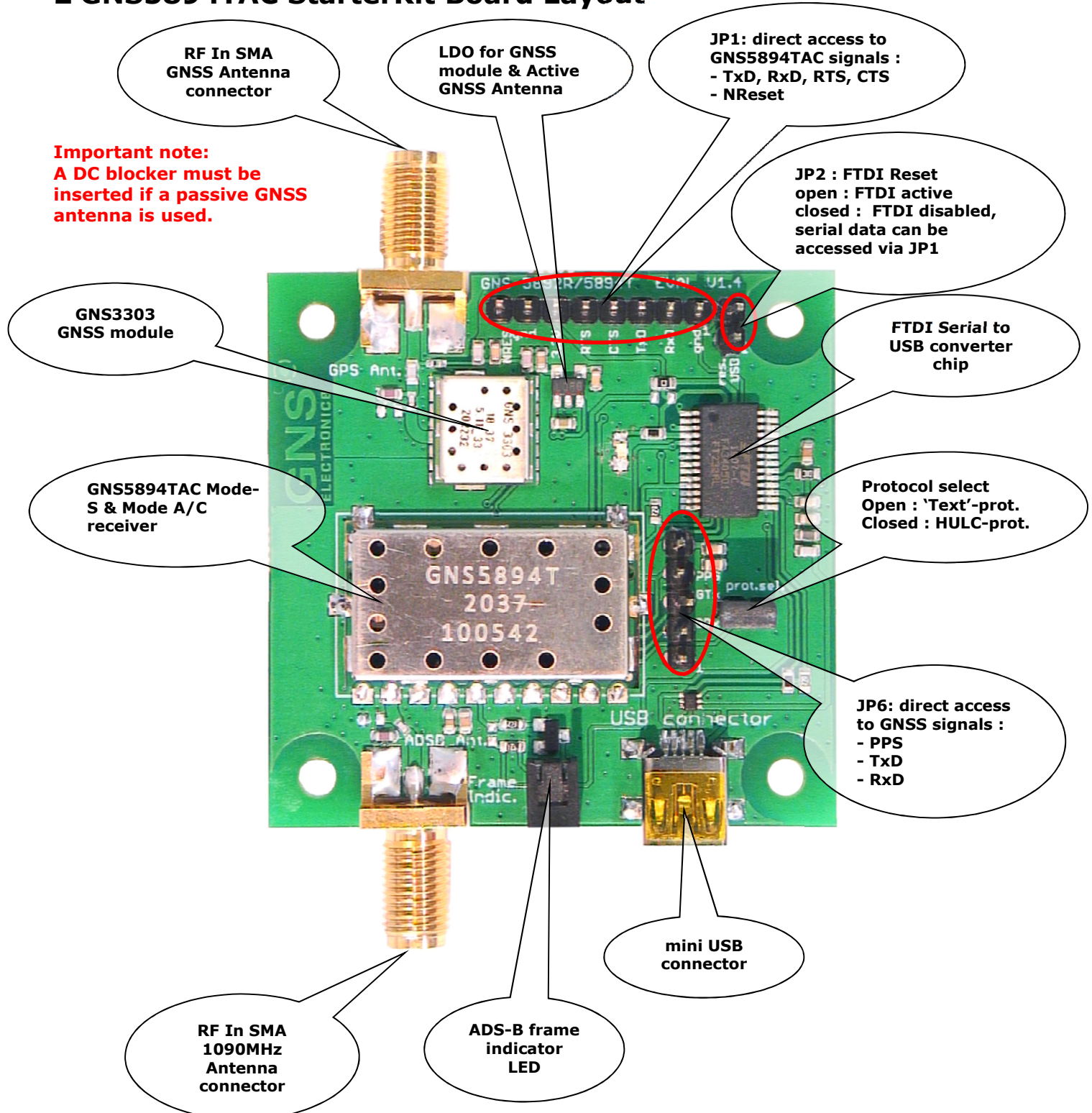
FEATURES

- Small evaluation board designed for verification of GNS 7000 / GNS 7040 module GNSS performance parameters
- Small evaluation board designed for easy implementation to MCU controlled applications
- DC supply via SMA connector
- GPS-fix LED indicator
- PC connection via USB
- USB powered



Typical use of the GNS5894TAC StarterKit and connected external components

2 GNS5894TAC StarterKit Board Layout



JUMPER AND CONNECTOR DESCRIPTION

CONNECTOR	PIN	SIGNAL	DESCRIPTION
JP1		1: GND 2: RxD (GNS5894TAC ser. input) 3: TxD (GNS5894TAC ser. output) 4: CTS (H/W handshake CTS input) 5: RTS (H/W handshake RTS output) 6: 3.3V 7: GND 8: \RESET GNS5894TAC, active LOW	Direct access to GNS5894TAC UART. UART I/O Voltage is 3.3V.
JP2		1: GND 2: \RESET FTDI, active LOW	Open : Operation via USB Port Closed : USB to serial converter is disabled, ser. Port (JP1) can be accessed.
JP6		1: GND 2: RxD2 (ser.Data from GNSS module) 3: TxD2 (ser.Data to GNSS module) 4: PPS (PPS pulse from GNSS module) 5: 3.3V	Serial data connection with GNSS module. I/O voltage is 3.3V.
PROT.SEL.		1: PS input (pulled up internally) 2: GND	Open : 'Text' protocol (AVR mode) Closed : 'HULC' protocol

Description of ADS-B frame indicator:

ADS-B frame indicator

blinking

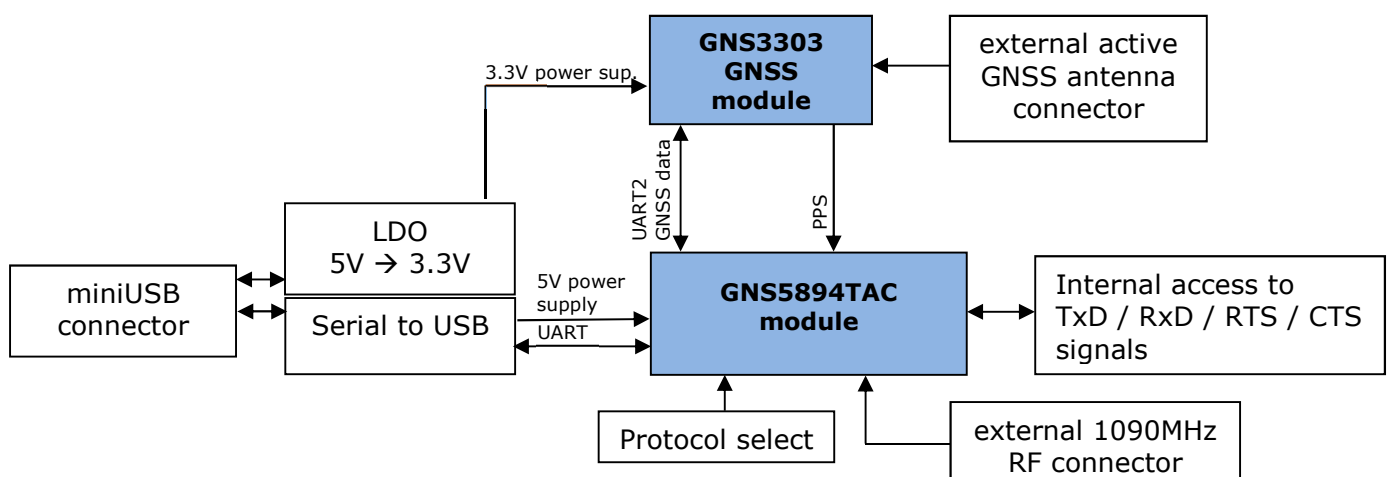
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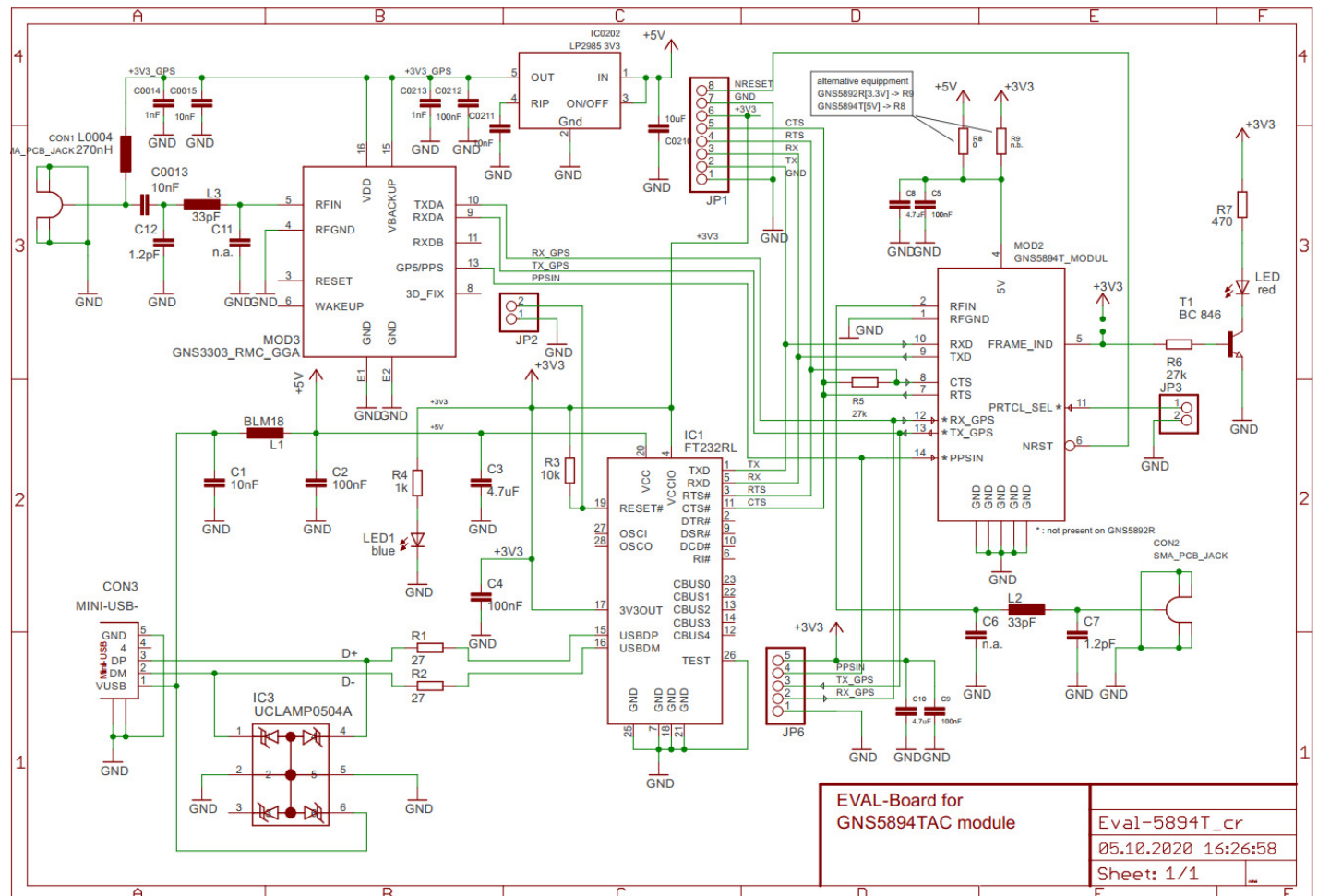
Mode-S and/or Mode-A/C frames are received and output via the serial interface.

No data is received (or the h/w handshake of the serial interface is not active).

3 GNS5894TAC StarterKit Board Block Diagram



4.2 5894TAC StarterKit Schematic



5 ORDERING INFORMATION

Ordering information			
Type	Part#		Description
GNS5894TAC StarterKit	t.b.d.		

6 RELATED DOCUMENTS

Type	description	Available from
T.B.D.		

7 DOCUMENT REVISION HISTORY

V0.9	10/05/2020	M.Heinzel	initial document